

```

(40) (((flash non-volatile nonvolatile EEPROM EPROM) and (buffer near2 (unit memory
(4784) (flash non-volatile nonvolatile (non adj volatile) EEPROM EPROM) and (buffer
(154) ((flash non-volatile nonvolatile (non adj volatile) EEPROM EPROM) and (buffer
(45) (((flash non-volatile nonvolatile (non adj volatile) EEPROM EPROM) and (buffer
(40) (((flash non-volatile nonvolatile (non adj volatile) EEPROM EPROM) and (buffer
(31) (((flash non-volatile nonvolatile (non adj volatile) EEPROM EPROM) and (buffer
(261786) ((concurrent$2 parallel simultaneous$2) with (stor$3 transfer$3 program$5 w
(2152) (flash non-volatile nonvolatile EEPROM EPROM) and (buffer volatile) and ((c
(18050) (flash non-volatile nonvolatile EEPROM EPROM) and (buffer volatile) and ((c
(6) (US-6145069-$ or US-6011741-$ or US-6549974-$).did. or (US-20030046483-$ or US-2
(0) ((US-6145069-$ or US-6011741-$ or US-6549974-$).did. or (US-20030046483-$ or US-2
(3) ((US-6145069-$ or US-6011741-$ or US-6549974-$).did. or (US-20030046483-$ or US-2
(2) ((US-6145069-$ or US-6011741-$ or US-6549974-$).did. or (US-20030046483-$ or US-2
(15126) 365/185.08 365/185.11 365/189.05 365/189.12 365/196 365/230.03 365/230.06 36
(9899) (365/185.08 365/185.11 365/189.05 365/189.12 365/196 365/230.03 365/230.06 36
(10429) (365/185.08 365/185.11 365/189.05 365/189.12 365/196 365/230.03 365/230.06 36
(532) (365/185.08 365/185.11 365/189.05 365/189.12 365/196 365/230.03 365/230.06 36
(531) (((365/185.08 365/185.11 365/189.05 365/189.12 365/196 365/230.03 365/230.06 36
(56) (((365/185.08 365/185.11 365/189.05 365/189.12 365/196 365/230.03 365/230.06 36
(50) (((365/185.08 365/185.11 365/189.05 365/189.12 365/196 365/230.03 365/230.06 36
(2) (((365/185.08 365/185.11 365/189.05 365/189.12 365/196 365/230.03 365/230.06 36
(3) (((365/185.08 365/185.11 365/189.05 365/189.12 365/196 365/230.03 365/230.06 36

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DB: USPAT.USPGPUB.EPO.IPO.DERVENT.18M.108
 Default operator: OR
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((((((365/185.08 365/185.11 365/189.04 365/189.05
 365/189.12 365/196 365/230.03 365/230.06 365/230.08 365/236
 365/239 365/240).ccls.) and controller and buffer and
 (nonvolatile non-volatile)) and (bank block array)) and
 ((concurrent\$2 simultaneous\$2 parallel) near2 (stor\$3)))
 and ((status adj2 register) flag register)) and (second
 adj2 register)

	U		Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	S	C						
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6421279 B1	20020716	137	Flash memory control method and apparatus processing	365/189.01	365/189.04; 365/189.05;		Tobita, Tsunehiro et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6026020 A	20000215	93	Data line disturbance free memory block divided flash	365/185.11	365/185.2; 365/185.29		Matsubara, Kiyoshi et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030048659 A1	20030313	19	Non-volatile memory device and data storing method	365/185.08			Kanamori, Motoki et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

Ready

ALM